

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	}	Group Art Unit:
<b>Hartman, Charles, et. al.</b>	}	<b>2111</b>
	}	
Serial No.:	}	Examiner:
<b>10/629,940</b>	}	<b>Daley, Christopher</b>
	}	
Filed:	}	
<b>July 29, 2003</b>	}	
	}	
For:	}	
<b>Configurable I/O Bus Architecture</b>	}	

RESPONSE TO OFFICE ACTION MAILED 5/19/2006

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

This paper responds to the office action that was mailed on 5/19/2006.

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